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FEB 27 2006**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of: Amir Alon, et al.

Docket No. IL920020007US1

Serial No.: 10/091,934

Examiner: Naum B Levin

Filed: March 6, 2002

Group Art: 2825

For: An Interconnect-Aware Methodology for Integrated Circuit Design

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450
(571) 273-8300

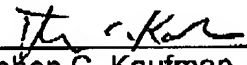
**CERTIFICATION OF FACSIMILE TRANSMISSION
PURSUANT TO 37 CFR 1.8**

I hereby certify that the following papers are being facsimile transmitted to the
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Respectfully submitted,

Dated: February 27, 2006



Stephen C. Kaufman, Esq.
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PLEASE FAX CONFIRMATION TO: Fax: 914-945-3281

IBM Corporation
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Yorktown Heights, New York 10598

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Amir Alon, et al.

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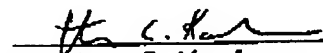
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Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450
(571) 273-8300ASSOCIATE POWER OF ATTORNEY

Dear Sir:

The undersigned attorney, who has been appointed as an attorney in the combined Declaration and Power of Attorney in the above-identified application, hereby appoints **Heidi Brun, Reg No. 34,504, of Heidi Brun Associates Ltd, Bet Shemesh, Isarel 99512, 972-2-991-9028**, as attorney to prosecute this application and transact all business in the Patent and Trademark Office connected therewith.

Respectfully submitted,


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P.O. Box 218
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